

MAR 08 2007

S/N 10/757,790

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Thomas J. Beacom	Examiner:	Paul Contino
Serial No.:	10/757,790	Group Art Unit:	2114
Filed:	January 15, 2004	Docket:	ROC920030257US1
Title:	Processor Error Detection During Processor Idle Cycles	Confirmation No.:	5443

AMENDMENT UNDER 37 CFR § 1.116

Filed via Fax  
Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

DO NOT ENTER: /PFC/

Sir:

In response to the Office Action dated January 8, 2007, please amend the subject application as follows.

Amendments To The Claims begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.